

IN THE CLAIMS:

Please amend the claims of this application so as to read as follows:

1. (Currently Amended) An active-matrix liquid crystal display apparatus comprising:
- an active-matrix substrate including a
 - plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements;
 - a counter electrode substrate including a
 - counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes;
 - a liquid crystal sandwiched between the
 - active-matrix substrate and the counter electrode substrate;
- the active-matrix substrate further
- including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines,

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the apparatus further comprising:

a supplementary capacitance drive circuit for driving the supplementary capacitance lines so that a predetermined potential difference ~~from a~~ between the voltage applied to the counter electrode and the voltage applied to the pixel electrodes is always maintained when any of the pixel electrodes and supplementary capacitance lines leaks.

2. (Currently Amended) The active-matrix liquid crystal display apparatus of claim 1, wherein a display mode of the liquid crystal display apparatus is normally-white and the supplementary capacitance drive circuit drives the supplementary capacitance so that a potential difference not less than a threshold voltage of the liquid crystal is maintained between the pixel electrodes and with respect to the counter electrode.

3. (Withdrawn) The active-matrix liquid crystal display apparatus of claim 1, wherein a display mode of the liquid crystal display apparatus is normally-black mode, and the supplementary capacitance drive circuit drives the supplementary capacitance lines so that a potential difference less than a threshold voltage of the liquid crystal is maintained from the counter electrode.

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4. (Currently Amended) The active-matrix liquid crystal display apparatus of claim 1,
- wherein the supplementary capacitance lines are separated from every scanning electrode line to which the switching element for switching-driving a pixel potential difference connected through the supplementary capacitance is connected at the intersection, and the supplementary capacitance drive circuit drives the supplementary capacitance lines with a polarity being reversed every time an on signal is input to the scanning electrode line driven at a stage preceding the scanning electrode line.
5. (Currently Amended) The active-matrix liquid crystal display apparatus of claim 2,
- wherein the supplementary capacitance lines are separated from every scanning electrode line to which the switching element for switching-driving a pixel potential difference connected through the supplementary capacitance is connected at the intersection, and the supplementary capacitance drive circuit drives the supplementary capacitance lines with a polarity being reversed every time an on signal is input to the scanning electrode line driven at a stage preceding the scanning electrode line.
6. (Withdrawn) The active-matrix liquid crystal display apparatus of claim 3, wherein the supplementary capacitance lines are separated every scanning electrode line to which the switching element for switch-driving a pixel potential difference connected through the supplementary capacitance is connected at the intersection, and the supplementary capacitance drive circuit drives the supplementary capacitance lines with a polarity being reversed every time an on signal is input to the scanning electrode line driven at a stage preceding the scanning electrode line.

SUB 17

7. (Original) The active-matrix liquid crystal display apparatus of claim 1, wherein the switching element and the pixel electrode are disconnected from each other at a pixel where the leakage between the pixel electrode and the supplementary capacitance line occurs.
8. (Original) The active-matrix liquid crystal display apparatus of claim 2, wherein the switching element and the pixel electrode are disconnected from each other at a pixel where the leakage between the pixel electrode and the supplementary capacitance line occurs.
9. (Withdrawn) The active-matrix liquid crystal display apparatus of claim 3, wherein the switching element and the pixel electrode are disconnected from each other at a pixel where the leakage between the pixel electrode and the supplementary capacitance line occurs
10. (Original) The active-matrix liquid crystal display apparatus of claim 4, wherein the switching element and the pixel electrode are disconnected from each other at a pixel where the leakage between the pixel electrode and the supplementary capacitance line occurs.

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11. (Currently Amended) A method for driving an active-matrix liquid crystal display apparatus comprising an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements; a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes; and a liquid crystal sandwiched between the active-matrix substrate and the counter electrode substrate, the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines, the method comprising:
- employing a constitution in which display is carried out in normally-white mode, for the active-matrix liquid crystal display apparatus; and
- driving the supplementary capacitances so that a potential difference not less than a threshold voltage of the liquid crystal is always maintained ~~with respect to~~ between the counter electrode and the pixel electrodes when any of the pixel electrodes and supplementary capacitances lines leaks.

SUB 1

12. (Currently Amended) The method for driving an active-matrix liquid crystal display apparatus of claim 11, further comprising:
- separating the supplementary capacitance lines from
 - every scanning electrode line to which the switching element for switching-driving the pixel electrode connected through the supplementary capacitance is connected at the intersection; and
 - driving the supplementary capacitance lines with a polarity being reversed every time an on signal is input to the scanning electrode line which is driven at a stage preceding the scanning electrode line.
13. (Original) The method for driving an active-matrix liquid crystal display apparatus of claim 11, wherein the switching element and the pixel electrode are disconnected from each other at a pixel where the leakage between the pixel electrode and the supplementary capacitance line occurs.

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14. (Withdrawn) A method for driving an active-matrix liquid crystal display apparatus comprising an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements; a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes; and a liquid crystal sandwiched between the active-matrix substrate and the counter electrode substrate; the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines, the method comprising:
- employing a constitution in which display is carried out in normally-black mode, for the active-matrix liquid crystal display apparatus; and
- driving the supplementary capacitances so that a potential difference less than a threshold voltage of the liquid crystal is always maintained with respect to the counter electrode when any of the pixel electrodes and supplementary capacitance lines leaks.

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15. (Withdrawn) The method for driving an active-matrix liquid crystal display apparatus of claim 14, further comprising:
separating the supplementary capacitance lines
every scanning electrode line to which the switching element for switching-driving the pixel electrode connected through the supplementary capacitance is connected at the intersection; and
driving the supplementary capacitance lines with a polarity being reversed every time an on signal is input to the scanning electrode line which is driven at a stage preceding the scanning electrode line.
16. (Withdrawn) The method for driving an active-matrix liquid crystal display apparatus of claim 14, wherein the switching element and the pixel electrode are disconnected from each other at a pixel where the leakage between the pixel electrode and the supplementary capacitance line occurs.

SUBMIT

17. (Withdrawn) A method for manufacturing an

active-matrix liquid crystal display apparatus, comprising:

preparing an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements and a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes,

the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines;

sandwiching a liquid crystal between the active-matrix substrate and the counter electrode substrate;

forming a supplementary capacitance drive circuit and connecting the supplementary capacitance drive circuit to the supplementary capacitance lines to drive the supplementary capacitance lines so that a predetermined potential difference from a voltage applied to the counter electrode is always maintained when any of the pixel electrodes and supplementary capacitance lines leaks;

inspecting whether there is a defect on a side of the active-matrix substrate;

determining, in the case where there is a defect, which pixel electrode is affected by the defect; and

causing a supplementary capacitance connected to the pixel electrode determined to be affected by the defect to leak.

SUBSET

18. (Withdrawn) The method for manufacturing an active-matrix liquid crystal display apparatus of claim 17, further comprising:
disconnecting the pixel electrode determined to be affected by the defect from the switching element connected to the pixel electrode.

19. (Currently Amended) An active-matrix liquid crystal display apparatus comprising:
an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrodes via the switching elements;
a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes;
a liquid crystal sandwiched between the active-matrix substrate and the counter electrode substrate;
the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines,
the apparatus further comprising:
a supplemental capacitance drive circuit for driving the supplemental capacitance lines so that a predetermined potential difference from between the pixel elements and a voltage applied to the counter electrode is always maintained.

SUBMIT

20. (Currently Amended) An active-matrix liquid crystal display apparatus comprising:

an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements;

a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes;

a liquid crystal sandwiched between the active-matrix substrate and the counter electrode substrate;

the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines,

the apparatus further comprising:

a supplemental capacitance drive circuit including a reference input maintained at the same potential as that of the common electrode for driving the supplemental capacitance lines so that a predetermined potential difference ~~from a voltage applied to~~ between the voltage applied to the counter electrode and the voltages applied to the pixel electrodes is always maintained when any of the pixel electrodes and supplemental capacitances ~~lines leaks, wherein the voltage applied to the counter electrode is applied to the supplemental capacitance drive circuit.~~

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